

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Patent Application Serial No. .... 09/920,978  
Filing Date ..... August 1, 2001  
Inventor ..... Shane J. Trapp  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2813  
Examiner ..... David S. Blum  
Attorney Docket No. .... MI22-1674  
Title: Method of Forming Integrated Circuitry and Method of Forming Shallow  
Trench Isolation in a Semiconductor Substrate

**Assistant Commissioner for Patents  
Washington, D.C. 20231**

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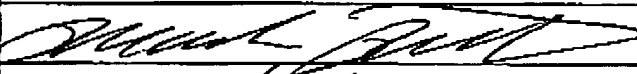
<b>TRANSMITTAL FORM</b>  (to be used for all correspondence after initial filing)	<b>Application Number</b>	09/920,978
	<b>Filing Date</b>	August 1, 2001
	<b>First Named Inventor</b>	Shane J. Trapp
	<b>Group Art Unit</b>	2813
	<b>Examiner Name</b>	David S. Blum
<b>Total Number of Pages in This Submission</b>		<b>Attorney Docket Number</b> MI22-1674

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